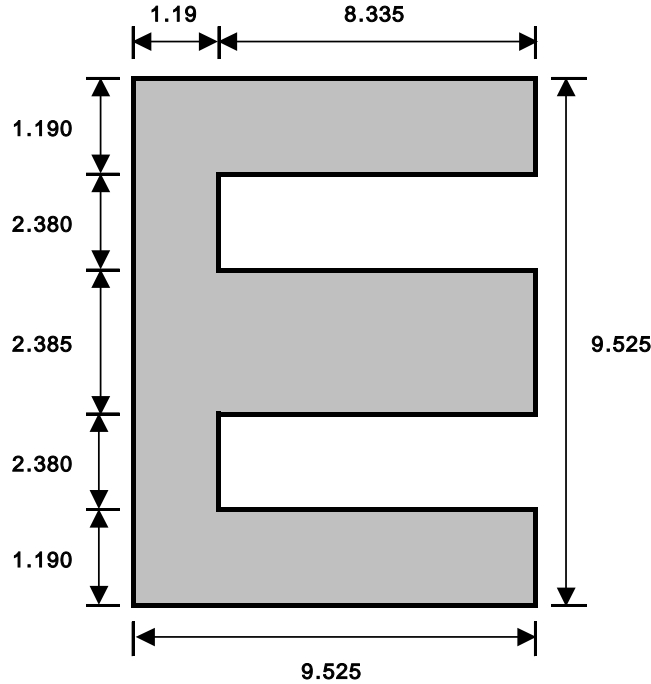




EE3031EL

SYEC CORE



TOLERANCE ± 0.08

PROPERTIES OF SQUARE STACK (10EA)

MAGNETIC DESIGN FORMULAE

$$B_{max} = \frac{7986 \cdot 10^3}{K_1 N} \text{ gauss per Volt at 60Hz}$$

(N is number of turns)

$$H_0 = (0.527 \cdot 10^{-3}) N \text{ Oersted per milliampere}$$

of direct current in winding

$$L = (0.0248 \cdot 10^{-8}) K_1 N^2 \mu \text{ ac Henries}$$

MAGNETIC PATH DIMENSION

$$l = 2.38 \text{ cm}$$

$$A = 0.047 \text{ cm}^2$$

K_1 (STACKING FACTOR)

Thickness	Butt jointed	Interleaved one per layer
0.1	0.90	0.80
0.2	0.90	0.85
0.35	0.95	0.90

MATERIAL TYPE	THICKNESS(mm)	WEIGHT & NUMBERS	
		GRAMS/PCS	PCS/KG
PC (NI 80%)	0.2T	0.0871	11,481